

## Intel Fpga Sdk For Opencil Altera

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**Running OpenCL™ on Intel® FPGAs** This course will cover the **Intel® FPGA** tools available to compile **OpenCL™** C code into **FPGA** hardware. We will discuss the ...

**Building Custom Platforms for Intel® FPGA SDK for OpenCL™: BSP Basics** This online training will introduce what a BSP is and describe the skills needed to build a custom platform. We will discuss ...

**Building an RTL Module for the Intel® FPGA SDK for OpenCL™** This online training will introduce the learner to building RTL (Verilog, SystemVerilog, or VHDL) modules for use in kernels ...

**OpenCL on Altera SoC FPGA (Linux Host) - Part 1 - Tools download and setup** Learn how to download, install and configure the tools required to develop **OpenCL** kernels and host code targeting **Altera** SoC ...

**Writing OpenCL™ Programs for Intel® FPGAs** This course will cover the constructs of the **OpenCL™** standard. You will learn about the platform, execution, memory, and ...

**Memory Optimization for OpenCL™ on Intel® FPGAs** This course covers memory optimization techniques for OpenCL™ solution on FPGAs. Learn an overview of global, constant, local ...

**Building Custom Platforms for Intel® FPGA SDK for OpenCL™: Modifying a Reference Platform** This online training will detail the flows involved when customizing a BSP reference platform and will go over which files ...

**Introduction to Parallel Computing with OpenCL™ on FPGAs** OpenCL™ is a standard for writing parallel programs for heterogeneous systems. In the **FPGA** environment, **OpenCL** constructs ...

**FPGA acceleration using Intel Stratix 10 FPGAs and OpenCL SDK - Supercomputing 2018, Dallas, Texas** How can FPGAs be used in HPC environments? We look at the hardware, development approaches, and a case study from Paderborn ...

**Intel FPGA Technical Training**

**How to package custom verilog modules or designs as OpenCL libraries** The video will discuss about why customers could potentially use this feature to have their custom processing blocks (RTL)in ...

**OPENCL #001 - How Install the Intel OpenCL Driver, NVIDIA GPU Computing (CUDA) Toolkit on Windows** We will learn how to install the Intel OpenCL driver on Windows. We will also install the NVIDIA GPU computing toolkit or CUDA ...

**OpenCL™ Coding Optimizations for Intel® Stratix® 10 Devices** In this course, we will cover how the offline kernel compiler of the Intel® FPGA SDK for OpenCL™ optimizes OpenCL kernel code ...

**CNN and GZIP implementation on FPGA by OpenCL** Convolutional Neural Network (CNN) is a Deep Learning algorithm used for various object classification. This AlexNet demo ...

**OpenCL™ Development with the Acceleration Stack for Intel® Xeon® CPU with FPGA** The Acceleration Stack for **Intel® Xeon® CPU with FPGA** is a robust collection of software, firmware, and tools intended to make it ...

**Building custom platform for Intel FPGA SDK for OpenCL (FPGA Device: 10AX066H)** Sobel Filter Demonstration: Acceleration card is Inventec SmartNIC card with **Intel FPGA** device 10AX066H Frame Size: 768x432 ...

**Using Channels and Pipes with OpenCL™ on Intel® FPGAs** This course covers using channels and pipes with OpenCL™ kernels written for Intel® FPGAs. The advantages of this methodology ...

**Building an Accelerator Functional Unit for the Intel® FPGA Programmable Acceleration Card N3000** In this training you will learn about building an accelerator functional unit (AFU) for the Intel® FPGA programmable ...

**Hello World example with Intel FPGA for OpenCL (BSP Device: 10AX066H)** Demonstration: Acceleration card is Inventec SmartNIC card with **Intel FPGA** device 10AX066H.

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